



Brian D. Ogonowsky

Carmen C. Cook

David C. Hsia

Rachel V. Leiterman

September 18, 2003

Attorney Docket No. MIC-M082

Mail Stop Patent Application  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

22386 U.S. PRO  
210/665645  
09/18/03

Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Thomas S. Wong, Robert W. Bechdolt, Phi Thai  
Title: Integrated Schottky Transistor Logic Configuration

X Return Receipt Postcard  
X This Transmittal Letter  
9 Page(s) Specification including Claims and Abstract  
6 Sheet(s) of Drawings (Figs. 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, and 12)  
2 Page(s) Declaration For Patent Application and Power of Attorney  
1 Page(s) Recordation of Assignment Cover Sheet  
2 Page(s) Assignment  
1 Page(s) Form PTO-1449  
1 Cited Reference

CLAIMS AS FILED

For	Number Filed	Number Extra	Rate	Basic Fee
Total Claims	15	-20	= 0	x \$ 18.00 = \$ 750.00
Independent Claims	1	-3	= 0	x \$ 84.00 = \$ 0.00
<input checked="" type="checkbox"/> Fee for Assignment Recordation				\$ 40.00

Please charge our Deposit Account No. 502226 in the amount of Total: \$ 790.00

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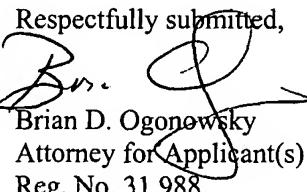
CORRESPONDENCE ADDRESS

Customer Number: 32566

EXPRESS MAIL LABEL NO:

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Respectfully submitted,

  
Brian D. Ogonowsky  
Attorney for Applicant(s)  
Reg. No. 31,988